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Applicant(s) Kazuo MATSUZAKI et al.	
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[illegible][illegible]

L o k e		<p>"Lowered on-state resistance of lateral power MOSFET with high withstand voltage for Intelligent Power Device"; Uno et al.; The Institute of Electrical Engineers of Japan, EDD-93-21, SPC-93-43; pgs. 21-29; October 7, 1993</p>

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Form PTO-A820
(also form PTO-1449)

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